•addon

1445502F4C-AO

ADTRAN[®] 1445502F4C Compatible TAA 40GBase-PLR4 QSFP+ Transceiver (SMF, 1310nm, 10km, MPO, DOM)

Features

- SFF-8436 Compliance
- MPO Connector
- Commercial Temperature 0 to 70 Celsius
- Single-mode Fiber
- Hot Pluggable
- Excellent ESD Protection
- Metal with Lower EMI
- RoHS Compliant and Lead Free



Applications

- 40GBase Ethernet
- Access and Enterprise

Product Description

This ADTRAN® 1445502F4C compatible QSFP+ transceiver provides 40GBase-PLR4 throughput up to 10km over single-mode fiber (SMF) using a wavelength of 1310nm via an MPO connector. It is guaranteed to be 100% compatible with the equivalent ADTRAN® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. – made or designated country end products."



Rev. 120921

Regulatory Compliance

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

Absolute Maximum Ratings

Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage	Vcc	-0.5		4.0	V
Storage Temperature	Ts	-40		85	°C
Operating Case Temperature	Тс	0	25	70	°C
Relative Humidity	RH	5		95	%
Data Rate Per Channel			10.3125	11.2	Gb/s

Electrical Characteristics

Parameter		Symbol	Min.	Тур.	Max.	Unit	Notes
Supply Voltage		V _{CC}	3.135	3.3	3.465	V	
Module Supply Current		lcc			1100	mA	
Power Dissipation		PD			3500	mW	
Transmitter							
Input Differentia	al Impedance	Z _{IN}		100			
Differential Data Input Swing		V _{IN, P-P}	180		900	mV _{P-P}	
TX_FAULT	Transmitter Fault	VOH	2.0		V _{CCHOST}	V	
	Normal Operation	VOL	0		0.8	V	
TX_DISABLE	Transmitter Disable	VIH	2.0		VCCHOST	V	
	Transmitter Enable	VIL	0		0.8	V	
Receiver							
Output Differential Impedance		ZO		100			
Differential Data Output Swing		VOUT, P-P	300		850	mV _{P-P}	1
Data Output Rise Time, Fall Time		t _r , t _f	28			ps	2
RX_LOS	Loss of signal (LOS)	VOH	2.0		V _{CCHOST}	V	3
	Normal Operation	VOL	0		0.8	V	3

Notes:

- 1. Internally AC coupled, but requires an external 100 differential load termination.
- 2. 20 80 %.
- 3. LOS is an open collector output. Should be pulled up with 4.7k on the host board.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes	
Transmitter							
Launch Optical Power per lane	Ро	-8.2		+0.5	dBm	1	
Side Mode Suppression Ratio	SMSR	30			dB		
Center Wavelength Range	٨0	1260	1310	1355	nm		
Extinction Ratio	EX	3.5			dB	2	
Optical Return Loss Tolerance	ORLT			12	dB		
Pout @TX-Disable Asserted	Poff			-30	dBm	1	
Receiver							
Center Wavelength	λς	1260		1355	nm		
Receiver Sensitivity (OMA)	S			-12.6	dBm	3	
Damage Threshold	POL	1.5			dBm	3	
LOS De-Assert	LOSD			-15	dBm		
LOS Assert	LOSA	-30			dBm		
LOS Hysteresis		0.5			dB		

Optical Characteristics

Note:

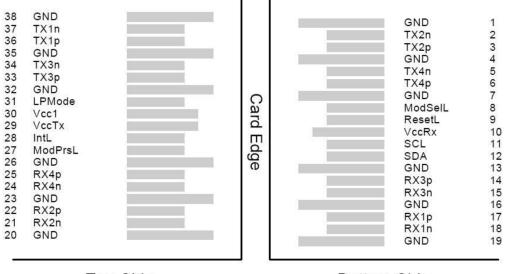
- 1. The optical power is launched into SMF.
- 2. Measured with a PRBS 2^{31} -1 test pattern @10.3125Gbps.
- 3. Measured with PRBS 2^{31} -1 test pattern, 10.3125Gb/s, BER<10⁻¹².

Pin Logic Symbol			Name/Descriptions			
1		GND	Module Ground	1		
2	CML-I	Tx2-	Transmitter inverted data input			
3	CML-I	Tx2+	Transmitter non-inverted data input			
4		GND	Module Ground	1		
5	CML-I	Tx4-	Transmitter inverted data input			
6	CML-I	Tx4+	Transmitter non-inverted data input			
7		GND	Module Ground	1		
8	LVTTL-I	MODSEIL	Module Select	2		
9	LVTTL-I	ResetL	Module Reset	2		
10		VCCRx	+3.3v Receiver Power Supply			
11	LVCMOS-I	SCL	2-wire Serial interface clock	2		
12	LVCMOS-I/O	SDA	2-wire Serial interface data	2		
13		GND	Module Ground	1		
14	CML-O	RX3+	Receiver non-inverted data output			
15	CML-O	RX3-	Receiver inverted data output			
16		GND	Module Ground	1		
17	CML-O	RX1+	Receiver non-inverted data output			
18	CML-O	RX1-	Receiver inverted data output			
19		GND	Module Ground	1		
20		GND	Module Ground	1		
21	CML-O	RX2-	Receiver inverted data output			
22	CML-O	RX2+	Receiver non-inverted data output			
23		GND	Module Ground	1		
24	CML-O	RX4-	Receiver inverted data output			
25	CML-O	RX4+	Receiver non-inverted data output			
26		GND	Module Ground	1		
27	LVTTL-O	ModPrsL	Module Present, internal pulled down to GND			
28	LVTTL-O	IntL	Interrupt output should be pulled up on host board	2		
29		VCCTx	+3.3v Transmitter Power Supply			
30		VCC1	+3.3v Power Supply			
31	LVTTL-I	LPMode	Low Power Mode	2		
32		GND	Module Ground	1		
33	CML-I	Tx3+	Transmitter non-inverted data input			
34	CML-I	Tx3-	Transmitter inverted data input			
35		GND	Module Ground	1		
36	CML-I	Tx1+	Transmitter non-inverted data input			
37	CML-I	Tx1-	Transmitter inverted data input			
38		GND	Module Ground	1		

Notes:

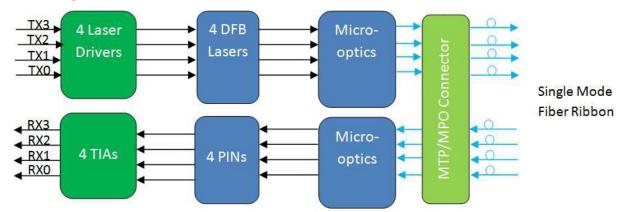
- 1. Module circuit ground is isolated from module chassis ground with in the module.
- Open collector; should be pulled up with 4.7k-10k ohms on host board to a voltage between 3.15V and 3.6V.

Electrical Pin-out Details



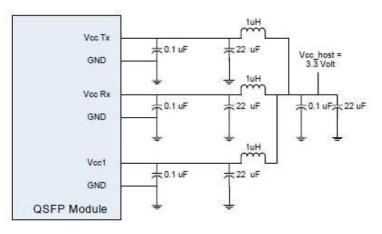
Top Side Viewed from Top

Bottom Side Viewed from Bottom

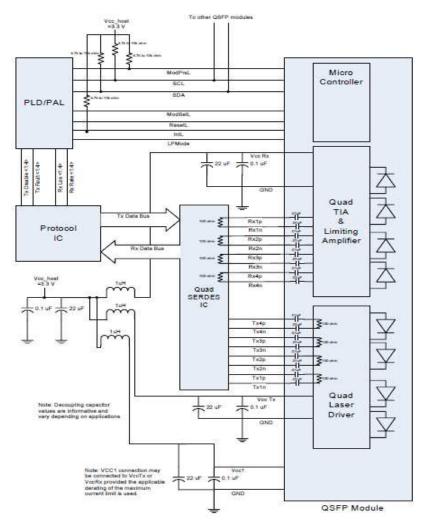


Transceiver Diagram Block

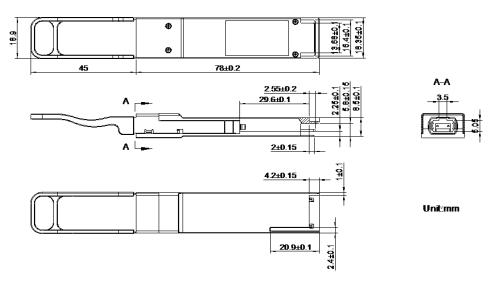
Recommended Host Board Power Supply Filter Network



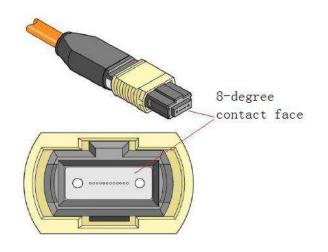
Recommended Application Interface Block Diagram



Mechanical Specifications



Attention: To minimize MPO connection induced reflections, an MPO receptacle with 8-degree angled end-face is utilized for this product. A female MPO connector with 8-degree end-face should be used with this product as illustrated in below Figure.



About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is in engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.

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